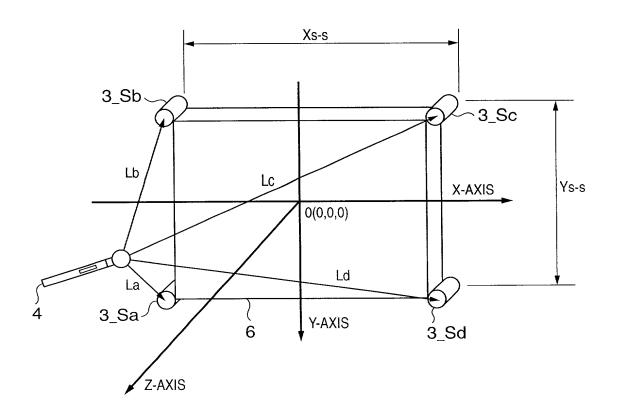


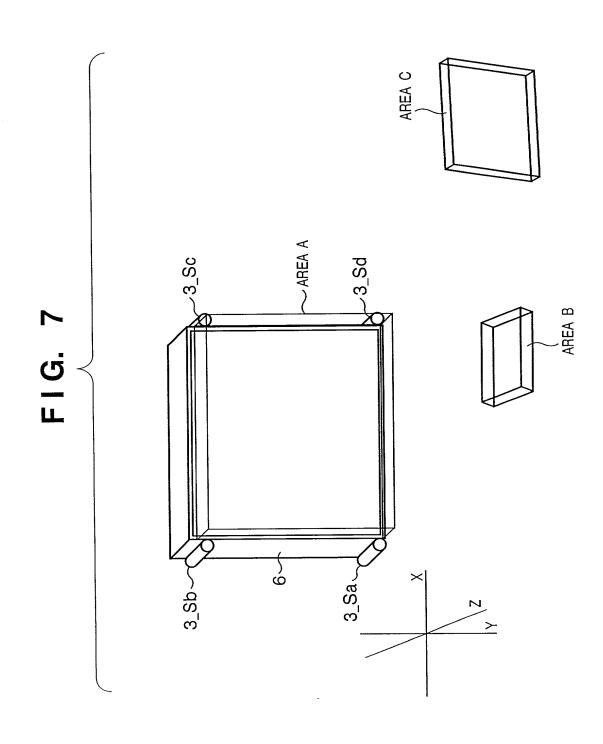
ARITHMETIC CONTROL CIRCUIT 86 Tg SIGNAL GENERATING CIRCUIT Tp SIGNAL DETECTION CIRCUIT ENVELOPE CARDINAL POINT DETECTION CIRCUIT 85 GATE SIGNAL GENERATING CIRCUIT 8-84 ENVELOPE DETECTION CIRCUIT BANDPASS FILTER PREAMPLIFYING CIRCUIT 80 ~ | || 111

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LATCH CIRCUIT Tp_d SIGNAL WAVEFORM DETECTION CIRCUIT LATCH CIRCUIT 15_c DETECTION SIGNAL INPUT PORT Tp_c LATCH CIRCUIT 15_b TIMER Tp_b LATCH CIRCUIT 15_a F1G. 5 Tp_a DETERMINATION -(EXTERNAL CIRCUIT) MICROCOMPUTER START SIGNAL DETECTION CIRCUIT I/O PORT

FIG. 6





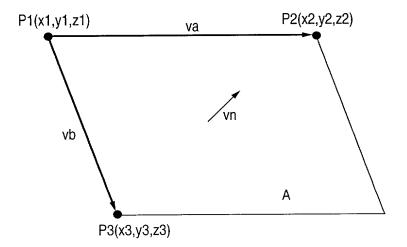


FIG. 9

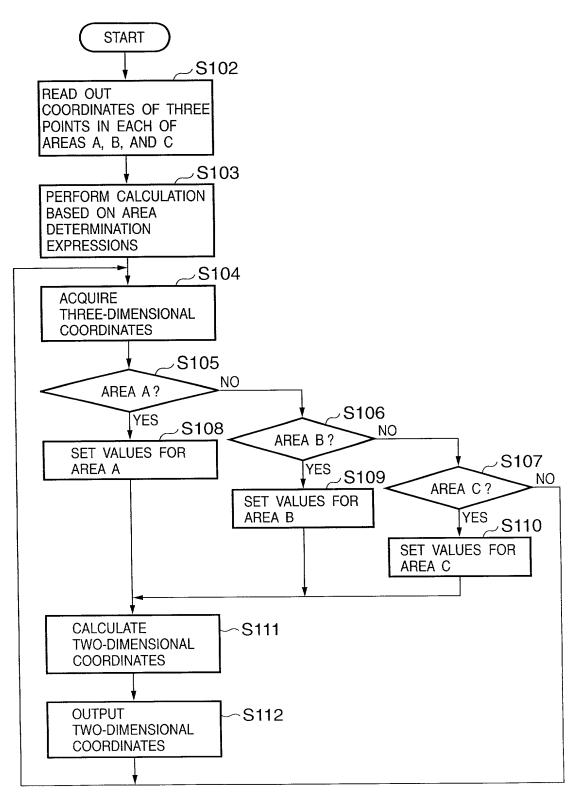
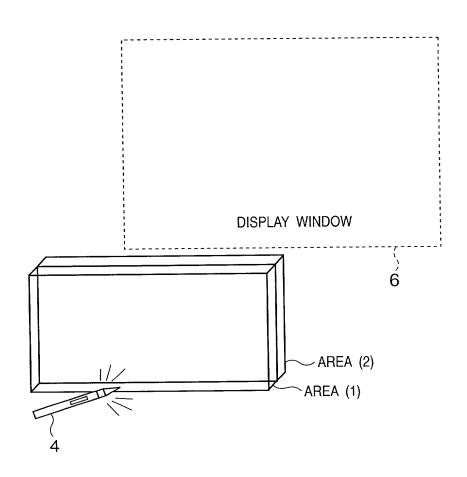
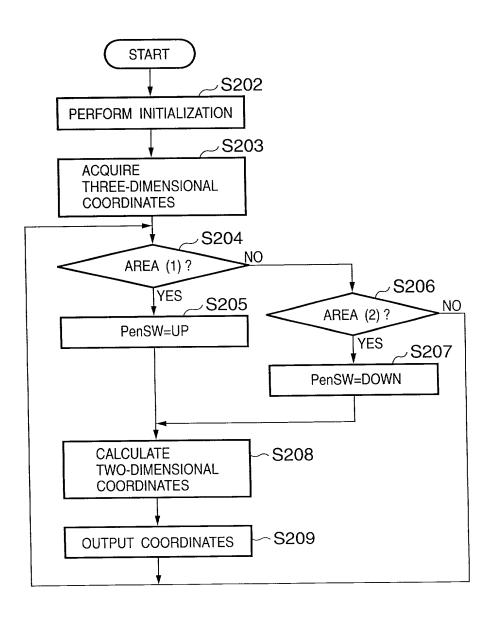
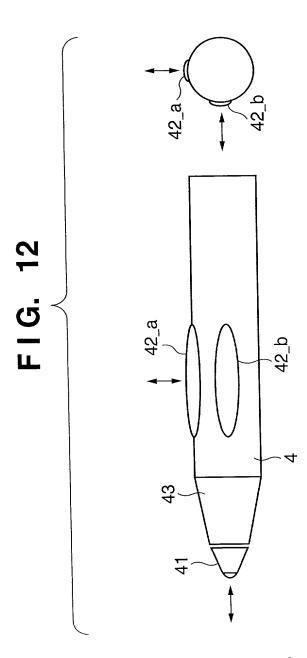


FIG. 10







| ABEA | PEN TIP SWITCH 41 | PEN SIDE SWITCH 42_a | PEN SIDE SWITCH 42_b | PEN DOWN SIGNAL |
|--------|-------------------|----------------------|----------------------|-----------------|
| ב ב | | | | TO CITY |
| AREA A | LEFT | LEFT | RIGHT | PEN IP SW |
| | | | | i |
| AREA B | DOUBLE CLICK | RIGHT | | SIDE SW1 |
| | | | | (|
| AREA C | — — | LEFT | RIGHT | DOWN |